



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 09/655,596 Confirmation No.: 9320  
Applicant : William F. Beausoleil, et al.  
Filing Date : September 6, 2000  
Title : High Speed Software Driven Emulator Comprised Of A Plurality of  
Emulation Processors With A Method To Allow Memory Read/Writes  
Without Interrupting The Emulation  
Group Art Unit : 2124  
Examiner : Tuan A. Vu  
Docket No. : 706316-1203  
Customer No. : 34313

Commissioner for Patents  
Mail Stop RCE  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO OFFICE ACTION**

Sir:

In response to the Office Action mailed November 15, 2005, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks begin on page 4 of this paper.

**CERTIFICATE OF MAILING**  
(37 C.F.R. §1.8a)

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

February 14, 2006  
Date of Deposit  
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Lois Jean Kimmerle  
Name of Person Mailing Paper

*Lois Jean Kimmerle*  
Signature of Person Mailing Paper-